

FIG. 1

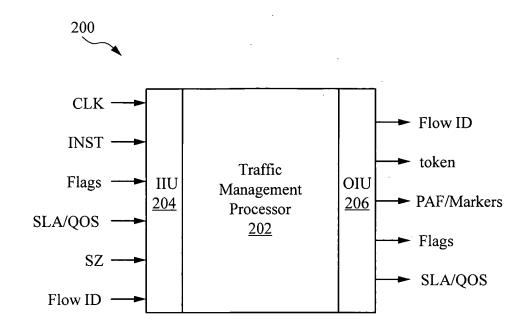


FIG. 2

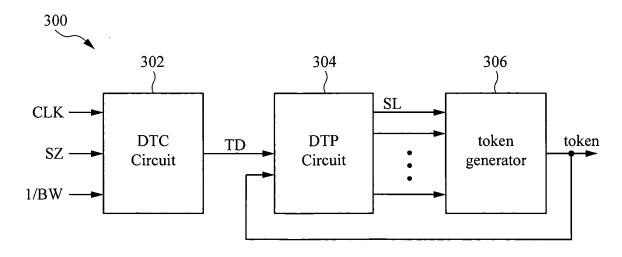


FIG. 3

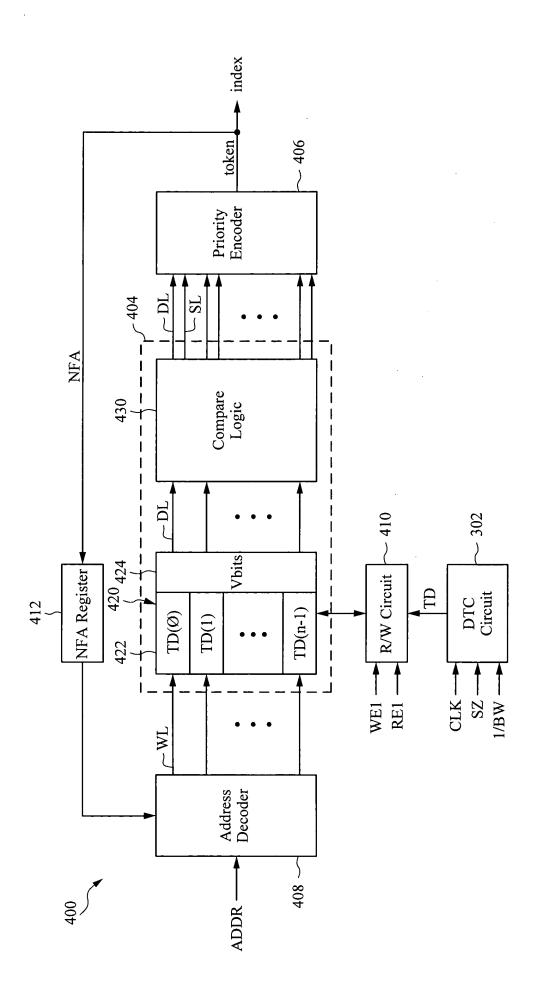
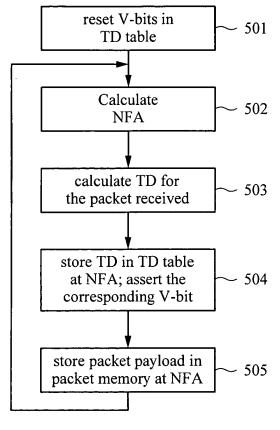


FIG. 4



forward TD's from - 51 table to compare compare TD's with each other to determine 512 which TD is the earliest assert corresponding - 513 select line generate index of row in table that - 514 stores the earliest TD de-assert V-bit - 515

FIG. 5A

FIG. 5B

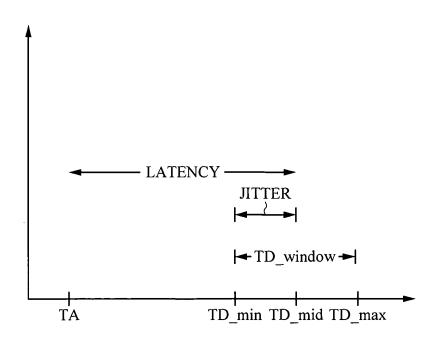
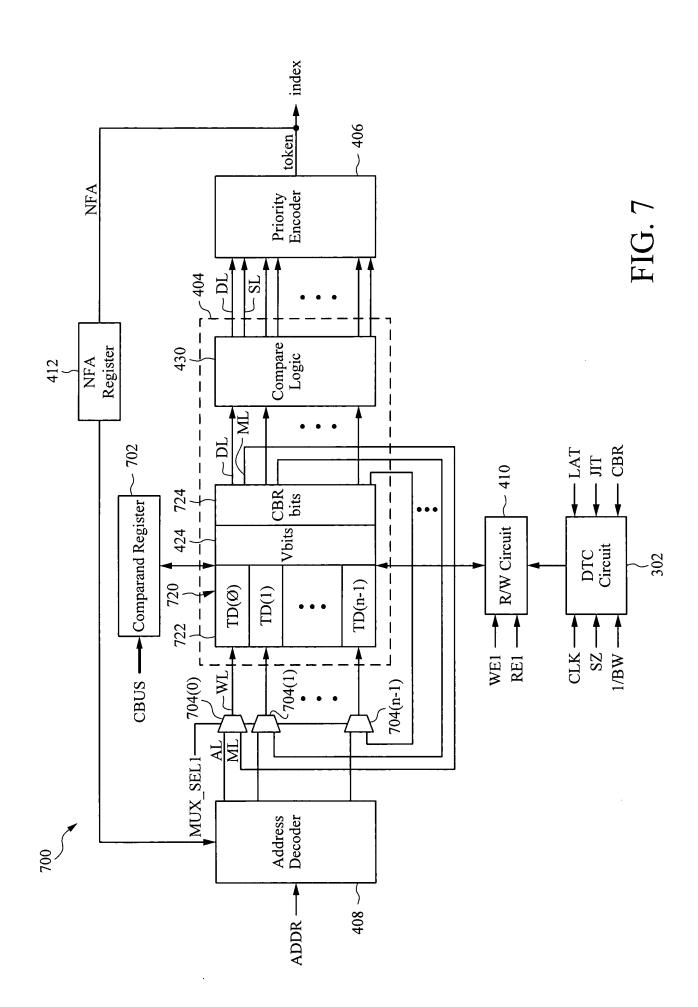


FIG. 6



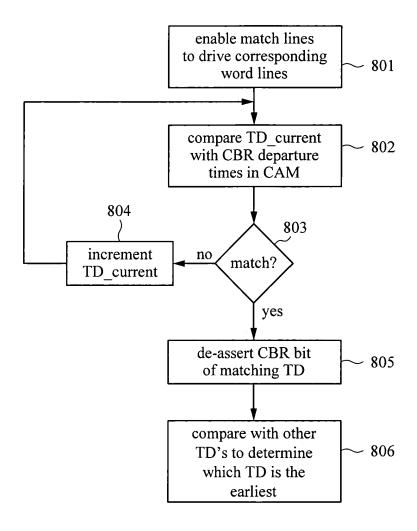


FIG. 8

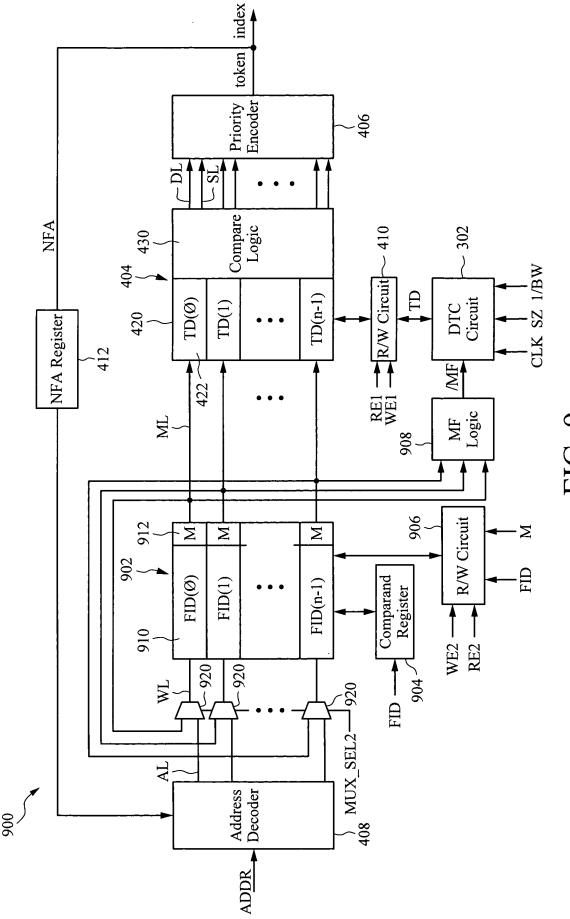


FIG. 9

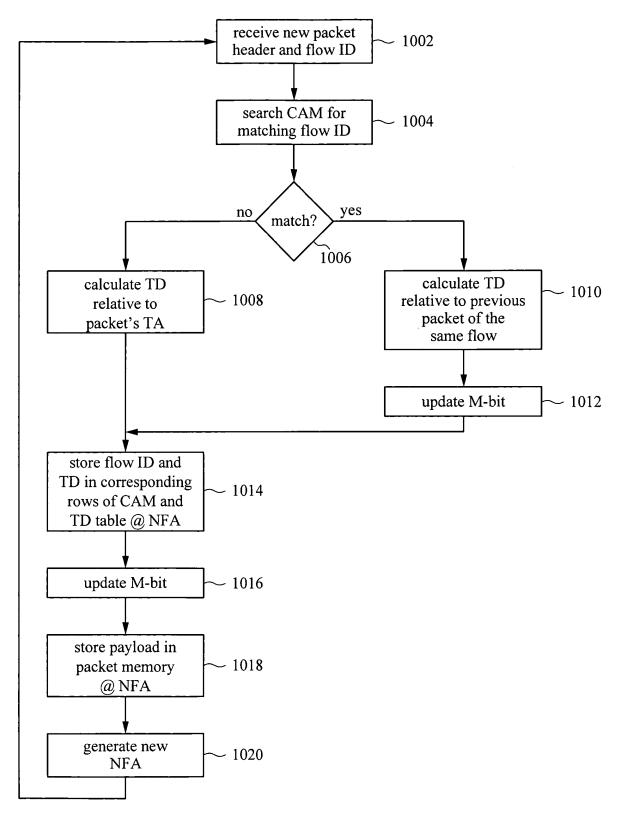


FIG. 10

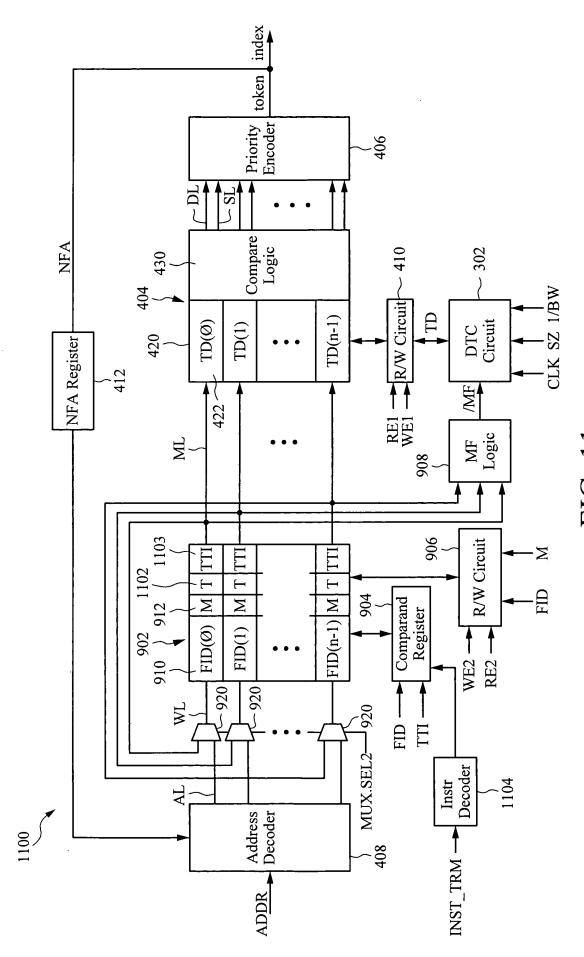
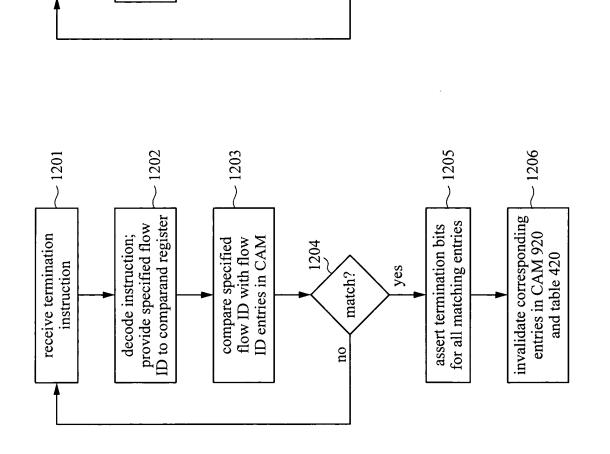


FIG. 11



1215

match?

0U

-1212

decode instruction; provide

receive termination

instruction

traffic type parameter to

comparand register

compare specified TTI

with TTI entries

in CAM

FIG. 12A

FIG. 12B

invalidate corresponding

entries in CAM 920

and table 420

for all matching entries

assert termination bits

yes

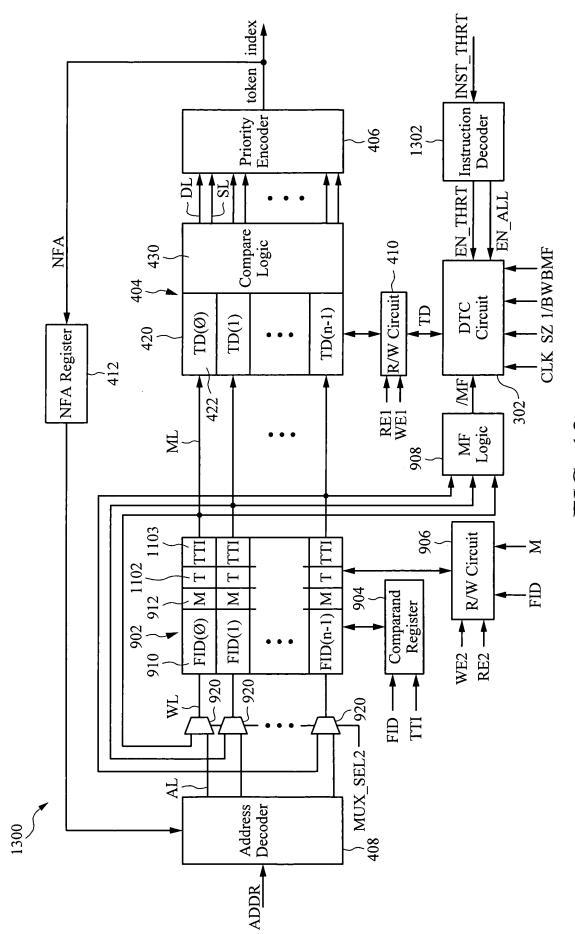


FIG. 13

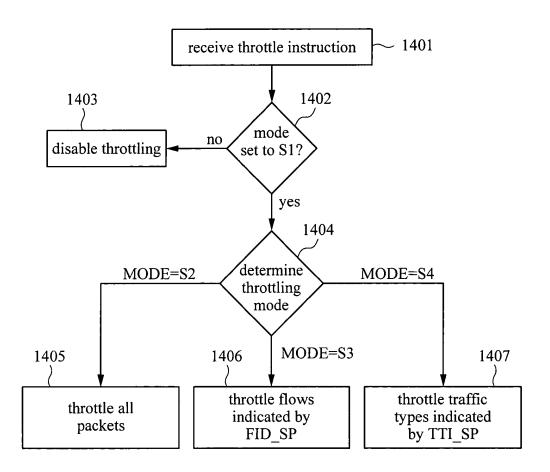
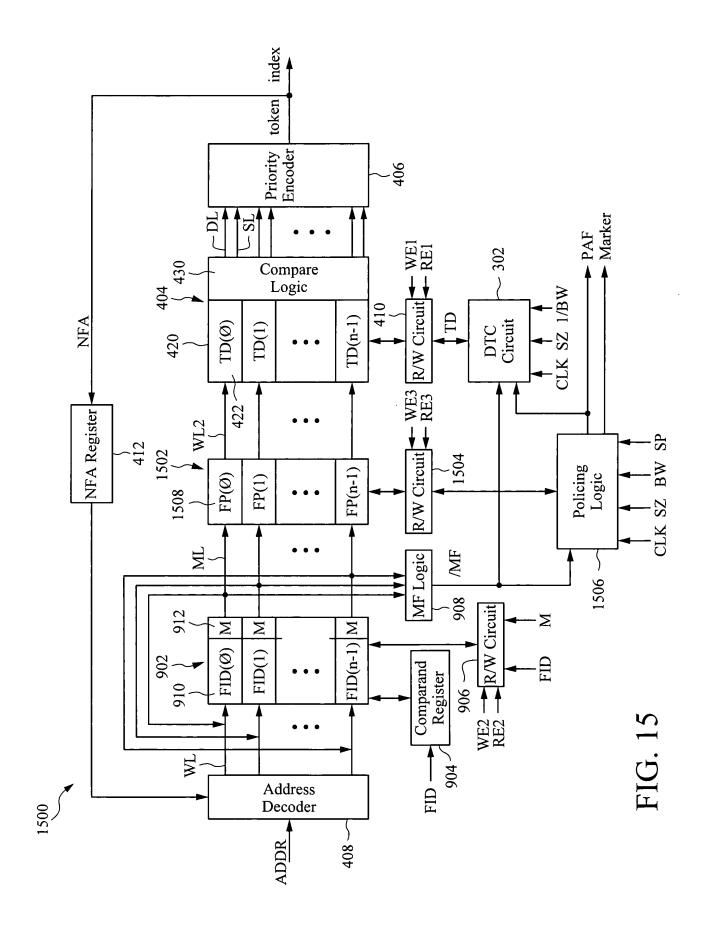


FIG. 14



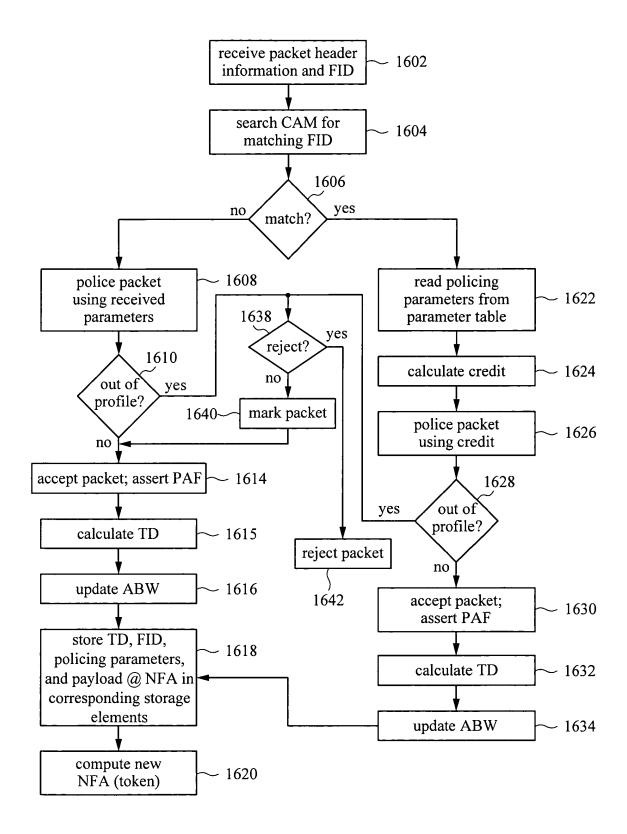
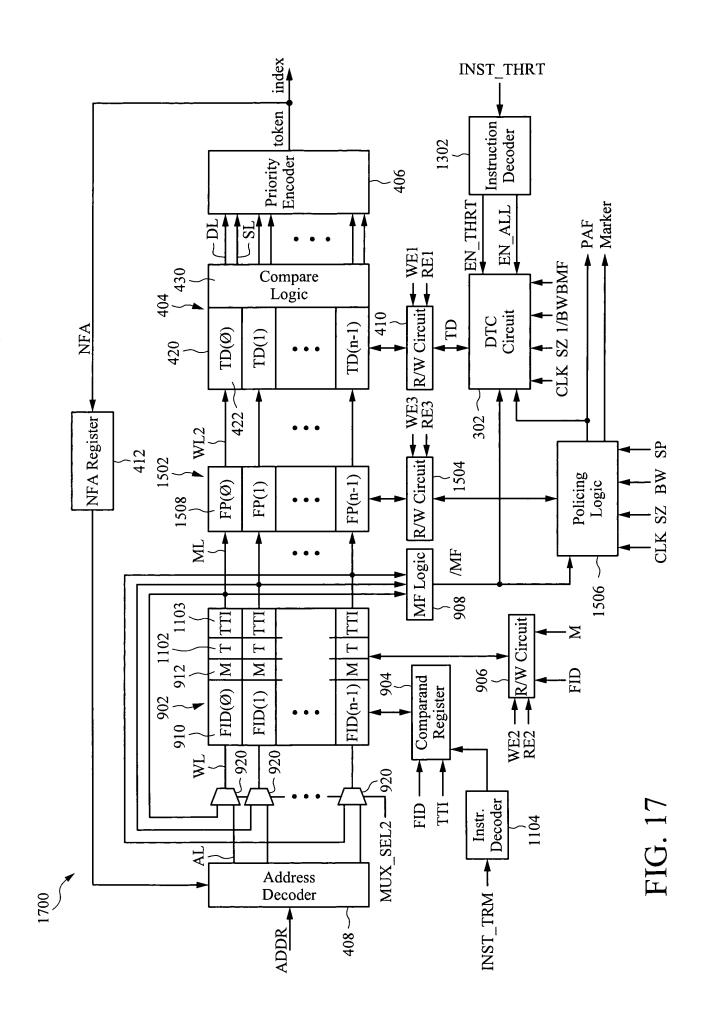


FIG. 16



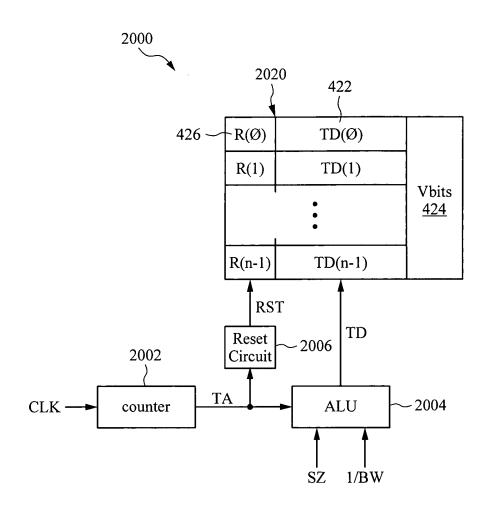


FIG. 18